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2817

PATENT  
Attorney Docket No. 401407

In re Application of: KANAYA et al.  
 Application No. 09/972,955  
 Filed: October 10, 2001  
 For: HIGH FREQUENCY SEMICONDUCTOR INTEGRATED CIRCUIT CAPABLE OF SWITCHING  
 BETWEEN CHARACTERISTICS

Mail Stop Non-Fee Amendment  
 Commissioner for Patents  
 P.O. Box 1450  
 Alexandria, VA 22313-1450

Sir:

Transmitted herewith is a response to an office action in the subject application.

Applicants claim small entity status of this application under 37 CFR 1.27.

Petition for Extension of Time
 

- Applicants petition for a one-month extension of time under 37 CFR 1.136, the fee for which is \$110.00 (enclosed).
- Applicants believe that no petition for an extension of time is necessary. However, to the extent that such petition is deemed necessary, Applicants hereby petition for a sufficient extension of time to render the present submission timely. Please charge Deposit Account No. 12-1216 for the appropriate petition fee.

No additional claim fee is required.

Other:

The claim fee has been calculated as shown below:

			HIGHEST NUMBER PREVIOUSLY PAID FOR	EXTRA CLAIMS PRESENT	SMALL ENTITY RATE	OTHER THAN A SMALL ENTITY RATE
TOTAL		MINUS		=	x 9=	\$
INDEPENDENT		MINUS		=	x 42=	\$
<input type="checkbox"/> FIRST PRESENTATION OF MULTIPLE CLAIM					+ 140=	\$
					TOTAL	\$
						TOTAL

Please charge my Deposit Account No. 12-1216 in the amount of \$ . A duplicate copy of this sheet is attached.

A check in the amount of \$ is attached.

The Commissioner is hereby authorized to charge any deficiencies in the following fees associated with this communication or credit any overpayment to Deposit Account No. 12-1216. A duplicate copy of this sheet is attached.
 

- Any filing fees under 37 CFR 1.16 for the presentation of extra claims.
- Any patent application processing fees under 37 CFR 1.17.

Respectfully submitted,

Jeffrey A. Wyand, Reg. No. 29,458  
 LEYDIG, VOIT & MAYER  
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Date: Sept 8, 2003  
 JAW:ves

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Attorney Docket No. 401407

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Application of:

KANAYA et al.

Art Unit: 2817

Application No. 09/972,955

Examiner: S. Jones

Filed: October 10, 2001

For: HIGH FREQUENCY  
SEMICONDUCTOR INTEGRATED  
CIRCUIT CAPABLE OF  
SWITCHING BETWEEN  
CHARACTERISTICS

**RESPONSE TO OFFICE ACTION**

Mail Stop Non-Fee Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

In response to the Office Action dated July 10, 2003, please enter the following amendments and consider the following remarks.

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